

5. (Previously Presented) A memory apparatus according to claim 1, characterized in that the second storage region is used as a region for storing a use history of the memory apparatus.

6. (Original) A memory apparatus according to claim 1, characterized in that the second storage region is used as a region for storing a quality history of the memory apparatus.

7. (Original) A memory apparatus according to claim 1, characterized by further comprising a read-only storage region storing address data of the second storage region.

8. (Currently Amended) A data-processing apparatus comprising data processing means for writing data into, and reading data from, [[an]] a memory apparatus comprising a first storage region from which data can be read and into which data can be written, in accordance with instructions made by a user, [[and]] a second storage region from which data can be read and into which data can be written by said data-processing apparatus, said second storage region having a user-use prohibition table which is normally inaccessible to said user and which has a plurality of addresses of data items in which one address designates one or more defective locations in said memory apparatus, and a conversion table which includes logic addresses which are assigned only to blocks in said first storage region and are not assigned to (i) defective blocks and (ii) non-defective blocks in said second storage region.] 2

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